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(71) Applicant (for all designated States except US): **INFINEON TECHNOLOGIES AG** [DE/DE]; St.-Martin-Strasse 53, 81669 Munich (DE).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **SANDERS, Anthony** [GB/DE]; Hugo Weiss Strasse 13, 81827 Munich (DE). **PRETE, Edoardo** [IT/DE]; Innere Wiener Strasse 16, 81667 Munich (DE).

(74) Agent: **CHARLES, Glyndwr**; Reinhard, Skuhra, Weise & Partner GbR, Friedrichstrasse 31, 80801 Munich (DE).

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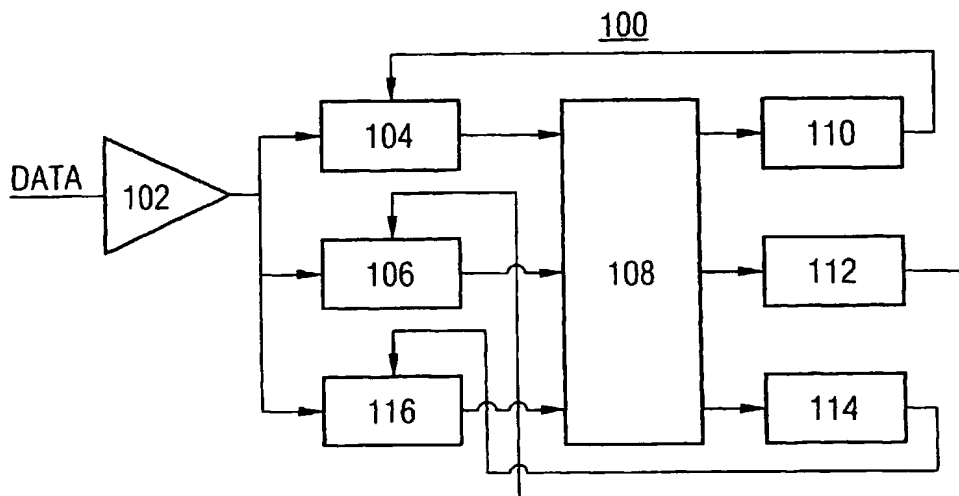
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(54) Title: **CLOCK SIGNAL EXTRACTION DEVICE AND METHOD FOR EXTRACTING A CLOCK SIGNAL FROM A DATA SIGNAL**



(57) **Abstract:** The invention provides a clock signal extraction device for extracting a clock signal from a periodic data signal, comprising a phase detector (104, 106) for detecting a first phase difference between rising edges of said data signal and a rising edges clock signal and for detecting a second phase difference between falling edges of said data signal and a falling edges clock signal; and a clock generator (110, 112) for generating said rising edges clock signal so that said first phase difference is minimized, for generating said falling edges clock signal so that said second phase difference is minimized, and for generating said clock signal in dependence on said first phase difference and said second phase difference. The invention further provides a method for extracting a clock signal from a periodic data signal.